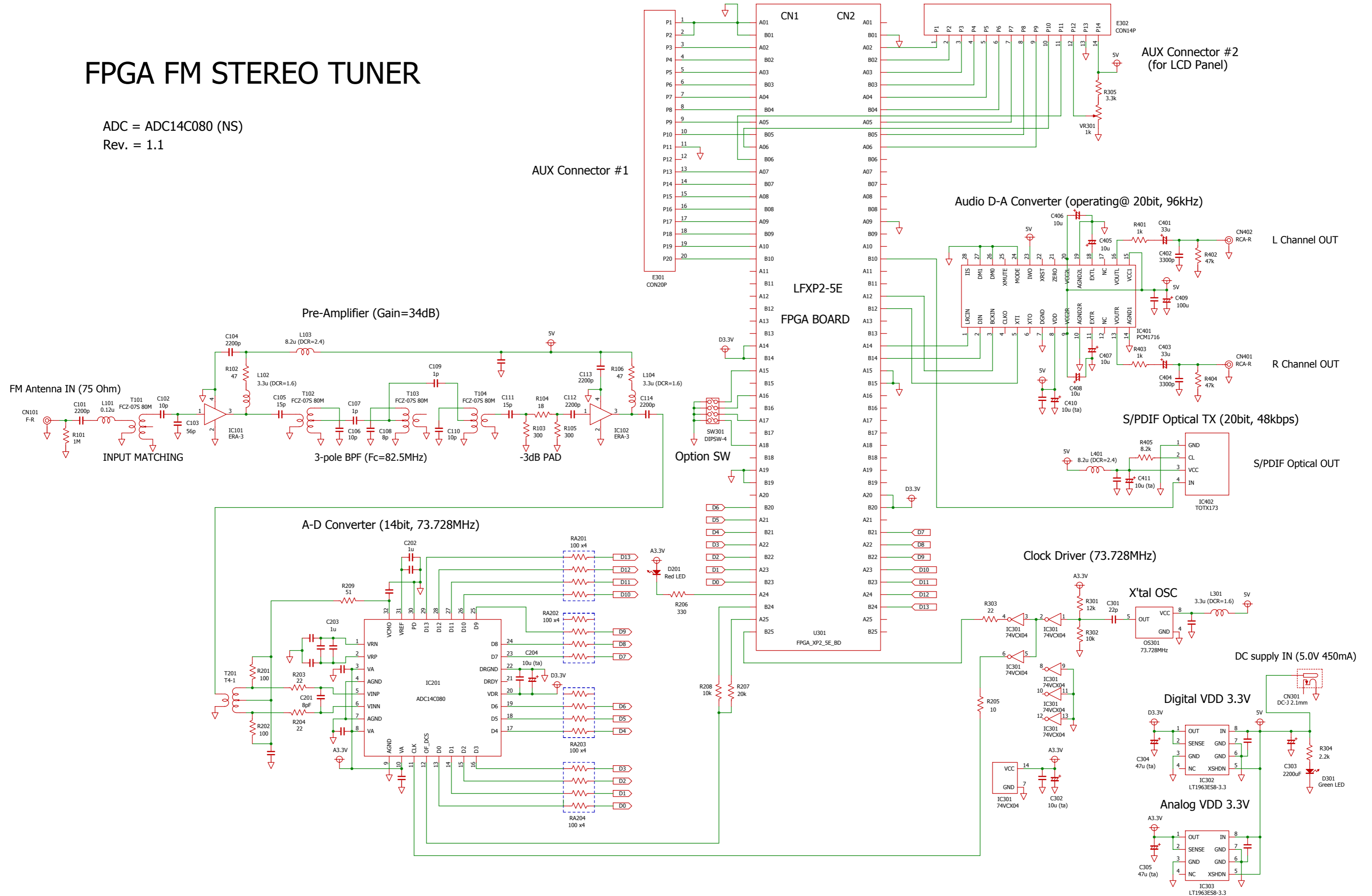


FPGA FM STEREO TUNER

ADC = ADC14C080 (NS)

Rev. = 1.1



AUX Connector #1

AUX Connector #2
(for LCD Panel)

Audio D-A Converter (operating @ 20bit, 96kHz)

Pre-Amplifier (Gain=34dB)

A-D Converter (14bit, 73.728MHz)

Clock Driver (73.728MHz)

X'tal OSC

Digital VDD 3.3V

Analog VDD 3.3V

LFXP2-5E

FPGA BOARD

Option SW

L Channel OUT

R Channel OUT

S/PDIF Optical OUT

DC supply IN (5.0V 450mA)

Caps w/o value = 100n, 1608, X7R