

FPGA FM STEREO TUNER

ADC = MAX12554 (MAXIM)
Rev. = 1.2

AUX Connector #1

AUX Connector #2 (for LCD Panel)

Audio D-A Converter (operating@ 20bit, 96kHz)

Pre-Amplifier (Gain=34dB)

FM Antenna IN (75 Ohm)

INPUT MATCHING

3-pole BPF (Fc=82.5MHz)

-3dB PAD

Option SW

A-D Converter (14bit, 73.728MHz)

Clock Driver (73.728MHz)

X'tal OSC

DC supply IN (5.0V 450mA)

Digital VDD 3.3V

Analog VDD 3.3V

Caps w/o value = 100n, 1608, X7R

